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Title of Invention:	METHOD AND SYSTEM FOR ABSORBING DEFECTS IN HIGH PERFORMANCE MICROPROCESSOR WITH A LARGE N-WAY SET ASSOCIATIVE CACHE			
First Named Inventor/Applicant Name:	David H. Asher			
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
Request for Certificate of Correction	Request for Cartificate of Correction	200301840.pdf	462145	no	2
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New International Application Filed with the USPTO as a Receiving Office

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